



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 10/748,564
First Inventor: Joseph B. Rowlands

Confirmation No.

Filed: 12/30/2003

Art Unit: 2171

Examiner:

Title: Internal Evict With External Request

Continuation of 09/909,009; filed 07/18/2001; now US Patent 6,697,918

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

Applicant submits the attached Information Disclosure Statement and requests the Examiner to consider the listed references. All of the references noted on this IDS were submitted on Form PTO-1449 by the Applicant or cited by the Examiner in the parent case, noted above. Thus, copies of the references are in the parent case.

It is certified, to the best of knowledge, this IDS is being submitted before the mailing date of a first Office Action on the merits. However, if there are any fee shortages related to this submission, please charge such fee shortages to Deposit Account No. 50-2126.

Respectfully submitted,

GARLICK, HARRISON & MARKISON, LLP
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**Form PTO-1449** (modified)
List of Patents and Publications
For Applicant's Information
Disclosure Statement
(Use several sheets if necessary)PATENT DKT. NO. **BP1833CON**SERIAL NO. **10/748,564**APPLICANT: **JOSEPH B. ROWLANDS**GROUP: **2171**FILING DATE: **12/30/03****U.S. PATENT DOCUMENTS**

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	A10	6,038,645	3/14/00	Nanda, et al.			

FOREIGN PATENT DOCUMENTS

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES/NO

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	A1	SiByte, "Target Applications," http://sibyte.com/mercurian/applications.htm , January 15, 2001, 2 pages.
	A2	SiByte, "SiByte Technology," http://sibyte.com/mercurian/technology.htm , January 15, 2001, 3 pages.
	A3	SiByte, "The Mercurian Processor," http://sibyte.com/mercurian , January 15, 2001, 2 pages.
	A4	SiByte, "Fact Sheet," SB-1 CPU, October 2000, rev. 0.1, 1 page.
	A5	SiByte, "Fact Sheet," SB-1250, October 2000, rev. 0.2, 10 pages.
	A6	Stepanian, SiByte, "SiByte SB-1 MIPS64 CPU Core, Embedded Processor Forum 2000, June 13, 2000, 15 pages.
	A7	Jim Keller, "The Mercurian Processor: A High Performance, Power-Efficient CMP for Networking," October 10, 2000, 22 pages.
	A8	Tom R. Halfhill, "SiByte Reveals 64-Bit Core For NPUs; Independent MIPS64 Design Combines Low Power, High Performance," Microdesign Resources, June 2000, Microprocessor Report, 4 pages.
	A9	Patterson and Hennessy, "Computer Architecture A Quantitative Approach," Second Edition, Morgan Kaufmann Publishers, Inc., 1990/1996, pp.375-378.

EXAMINER:

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Information Disclosure Statement--PTO 1449 (modified)

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Complete if Known

Application Number	10/748,564
Filing Date	12/30/03
First Named Inventor	Joseph B. Rowlands
Art Unit	2171
Examiner Name	
Attorney Docket Number	BP1833CON

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U. S. PATENT DOCUMENTS

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FOREIGN PATENT DOCUMENTS

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Examiner
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